

Features:

- 1. Popular T-1 3/4 diameter package.
- 2. High intensity.
- 3. CMOS technology.
- 4. Designed for bonding with LED chip.
- 5. 1/4 Duty cycle.
- 6. Blinking frequency: 2.4Hz (Vdd=3.50V).
- 7. Frequency tolerance: ± 20%.
- 8. With both sink and source output drivers.
- 9. The product itself will remain within RoHS compliant Version.

Descriptions:

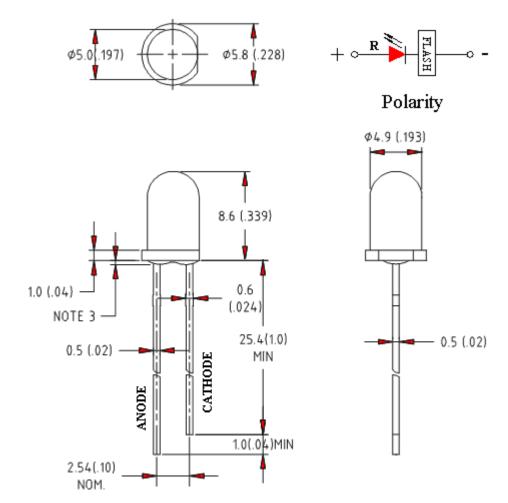
- 1. The series is specially designed for applications requiring higher brightness.
- 2. The LED lamps are available with different colors, intensities.

Applications:

- 1. Status indicators.
- 2. Commercial use.
- 3. Advertising Signs.
- 4. Monitor.
- 5. Telephone.
- 6. Computer.
- 7. Circuit board.
- 8. Entertainment.
- 9. Decoration.

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◆ Package Dimension:



Part No.	Chip Material	Lens Color	Source Color
DL-506SRC-F1	AlGaInP	Water Clear	Hyper Red

Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ± 0.25mm (.010") unless otherwise noted.
- 3. Protruded resin under flange is 1.00mm (.039") max.
- 4. Specifications are subject to change without notice.

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◆ Absolute Maximum Ratings at Ta=25°C

Parameters	Symbol	Max.	Unit
Power Dissipation	PD	180	mW
Peak Forward Current (1/10 Duty Cycle, 0.1ms Pulse Width)	IFP	100	mA
Continuous Forward Current	IF	30	mA
Derating Linear From 50°C		0.40	mA/°C
Reverse Voltage	VR	5	V
Operating Temperature Range	Topr	-40°C to +80°C	
Storage Temperature Range	Tstg	-40°C to +85°C	
Lead Soldering Temperature [4mm (.157") From Body]	Tsld	260°C for 5 Seconds	

◆ Electrical Optical Characteristics at Ta=25 °C

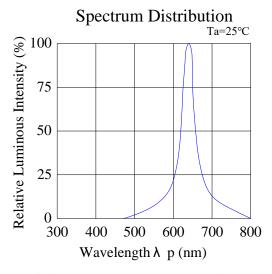
Parameters	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Luminous Intensity (Note 1) *	IV	1000	2000		mcd	V _{DD} =4.50V (Note 1)
Viewing angle	201/2		15		Deg	V _{DD} =4.50V (Note 2)
Peak Emission Wavelength	λр		632		nm	V _{DD} =4.50V
Dominant Wavelength (Note 2)	λd		624		nm	V _{DD} =4.50V
Spectral Line Half-Width	Δλ		20		nm	V _{DD} =4.50V
Turn on time	Duty		1/4		ms	V _{DD} =4.50V
Blinking Frequency	Fblk		2.4		Hz	V _{DD} =4.50V
Frequency Tolerance	Ft		±20%		Hz	V _{DD} =4.50V
Operating Voltage	Vdd	2.80	3.50	6.00	V	
Reverse Current	IR			50	μΑ	V _R =5V

Notes:

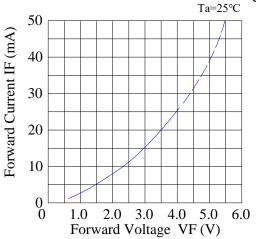
- 1. $\theta_{1/2}$ is the off-axis angle at which the luminous intensity is half the axial luminous intensity.
- 2. The dominant wavelength (λ d) is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.

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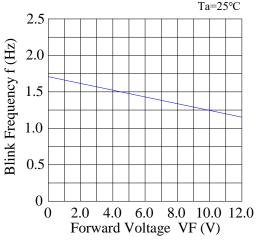
◆ Typical Electrical / Optical Characteristics Curves (25°C Ambient Temperature Unless Otherwise Noted)



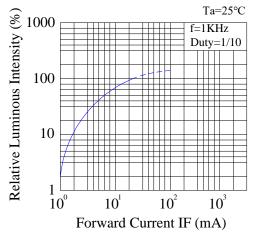
Forward Current & Forward Voltage



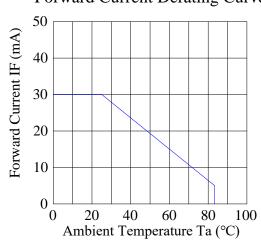
Blink Frequency & Forward Voltage



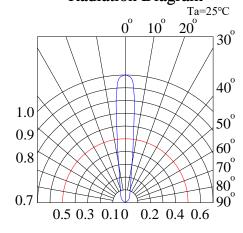
Luminous Intensity & Forward Current



Forward Current Derating Curve



Radiation Diagram



Reliability Test Items And Conditions:

The reliability of products shall be satisfied with items listed below:

Confidence level: 90%.

LTPD: 10%.

1) Test Items and Results:

Test Item	Standard Test Method	Test Conditions	Note	Number of Damaged
Resistance to Soldering Heat	JEITA ED-4701 300 302	Tsld=260±5°C, 10sec 3mm from the base of the epoxy bulb	1 time	0/100
Solder ability	JEITA ED-4701 300 303	Tsld=235±5℃, 5sec (using flux)	1time over 95%	0/100
Thermal Shock	JEITA ED-4701 300 307	0°C~100°C 15sec, 15sec	100 cycles	0/100
Temperature Cycle	JEITA ED-4701 100 105	-40°C~25°C~100°C~25°C 30min,5min,30min,5min	100 cycles	0/100
Moisture Resistance Cycle	JEITA ED-4701 200 203	25℃~65℃~-10℃ 90%RH 24hrs/1cycle	10 cycles	0/100
High Temperature Storage	JEITA ED-4701 200 201	Ta=100°C	1000hrs	0/100
Terminal Strength (Pull test)	JEITA ED-4701 400 401	Load 10N (1kgf) 10±1sec	No noticeable damage	0/100
Terminal Strength (bending test)	JEITA ED-4701 400 401	Load 5N (0.5kgf) 0°~90°~0° bend 2 times	No noticeable damage	0/100
Temperature Humidity Storage	JEITA ED-4701 100 103	Ta=60℃, RH=90%	1000hrs	0/100
Low Temperature Storage	JEITA ED-4701 200 202	Ta=-40°C	1000hrs	0/100
Steady State Operating Life		Ta=25℃, IF=30mA	1000hrs	0/100
Steady State Operating Life of High Humidity Heat		Ta=60℃, RH=90%, IF=30mA	500hrs	0/100
Steady State Operating Life of Low Temperature		Ta=-30℃, IF=20mA	1000hrs	0/100

2) Criteria for Judging the Damage:

ltem	Symbol	Test Conditions	Criteria for Judgment		
			Min	Max	
Forward Voltage	VF	IF=20mA		F.V.*)×1.1	
Reverse Current	IR	VR=5V		F.V.*)×2.0	
Luminous Intensity	IV	IF=20mA	F.V.*)×0.7		

*) F.V.: First Value.

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Please read the following notes before using the product:

1. Over-current-proof

Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).

Storage

- 2.1 Do not open moisture proof bag before the products are ready to use.
- 2.2 Before opening the package, the LEDs should be kept at 30℃ or less and 90%RH or less.
- 2.3 The LEDs should be used within a year.
- 2.4 After opening the package, the LEDs should be kept at 30°C or less and 70%RH or less.
- 2.5 The LEDs should be used within 168 hours (7 days) after opening the package.

3. Soldering Iron

Each terminal is to go to the tip of soldering iron temperature less than 260°C for 5 seconds within once in less than the soldering iron capacity 25W. Leave two seconds and more intervals, and do soldering of each terminal. Be careful because the damage of the product is often started at the time of the hand solder.

4. Repairing

Repair should not be done after the LEDs have been soldered. When repairing is unavoidable, a double-head soldering iron should be used (as below figure). It should be confirmed beforehand whether the characteristics of the LEDs will or will not be damaged by repairing.

Caution in ESD

Static Electricity and surge damages the LED. It is recommended to use a wrist band or anti-electrostatic glove when handling the LED. All devices equipment and machinery must be properly grounded.

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